

## Introduction To Vlsi Circuits And Systems

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Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up, paying much attention to logic circuit, layout, and system designs. Armed with these tools, readers can not only comprehensively understand the features and limitations of modern VLSI technologies, but also have enough background to adapt to this ever-changing field.

Introduction to VLSI Systems: A Logic, Circuit, and System...

Introduction to VLSI

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Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining thousands of transistors into a single chip. VLSI began in the 1970s when complex semiconductor and communication technologies were being developed. The microprocessor is a VLSI device.

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Introduction to VLSI Circuits Design. This note covers the following topics: Fabrication, MOSFET, SPICE MODEL, Inverters, Interconnect Analysis, Super Buffer Design, Combination Circuit Design, Sequential Logic Circuits , Dynamic Logic Circuits , Semiconductor Memories, Low-Power CMOS Logic Circuits. Author(s): National Chiao Tung University

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Introduction to VLSI Circuits and Systems illustrates the top-down design procedure used in modern VLSI chip design with an emphasis on variations in the HDL, logic, circuits and layout. This book provides a comprehensive treatment of modern VLSI design.

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Very large-scale integration is the process of creating an integrated circuit by combining millions of MOS transistors onto a single chip. VLSI began in the 1970s when MOS integrated circuit chips were widely adopted, enabling complex semiconductor and telecommunication technologies to be developed. The microprocessor and memory chips are VLSI devices. Before the introduction of VLSI technology, most ICs had a limited set of functions they could perform. An electronic circuit might consist of a

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Description: This book provides a comprehensive treatment of modern VLSI design. It stresses the relationship among high-level system considerations, logic design, and silicon circuitry and fabrication in a manner that allows the reader to understand the field as a single composite discipline.

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